

Figure 1

Day et al.
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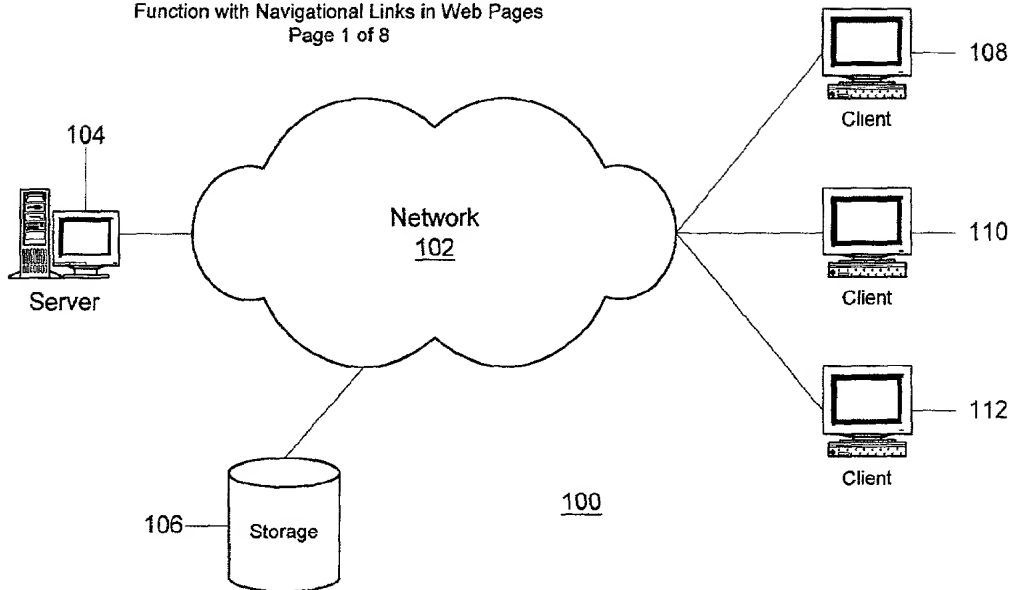


Figure 2 is a block diagram of a server 200. The server 200 includes a processor 202 and a processor 204. The processor 202 and processor 204 are connected to a system bus 206. The system bus 206 is connected to a memory controller/cache 208 and an I/O bridge 210. The memory controller/cache 208 is connected to local memory 209. The I/O bridge 210 is connected to an I/O bus 212. The I/O bus 212 is connected to a graphics adapter 230 and a hard disk 232. The I/O bus 212 is also connected to three PCI bus bridges: PCI Bus Bridge 214, PCI Bus Bridge 222, and PCI Bus Bridge 224. PCI Bus Bridge 214 is connected to a PCI bus 216, which is connected to a modem 218 and a network adapter 220. PCI Bus Bridge 222 is connected to a PCI bus 226. PCI Bus Bridge 224 is connected to a PCI bus 228.

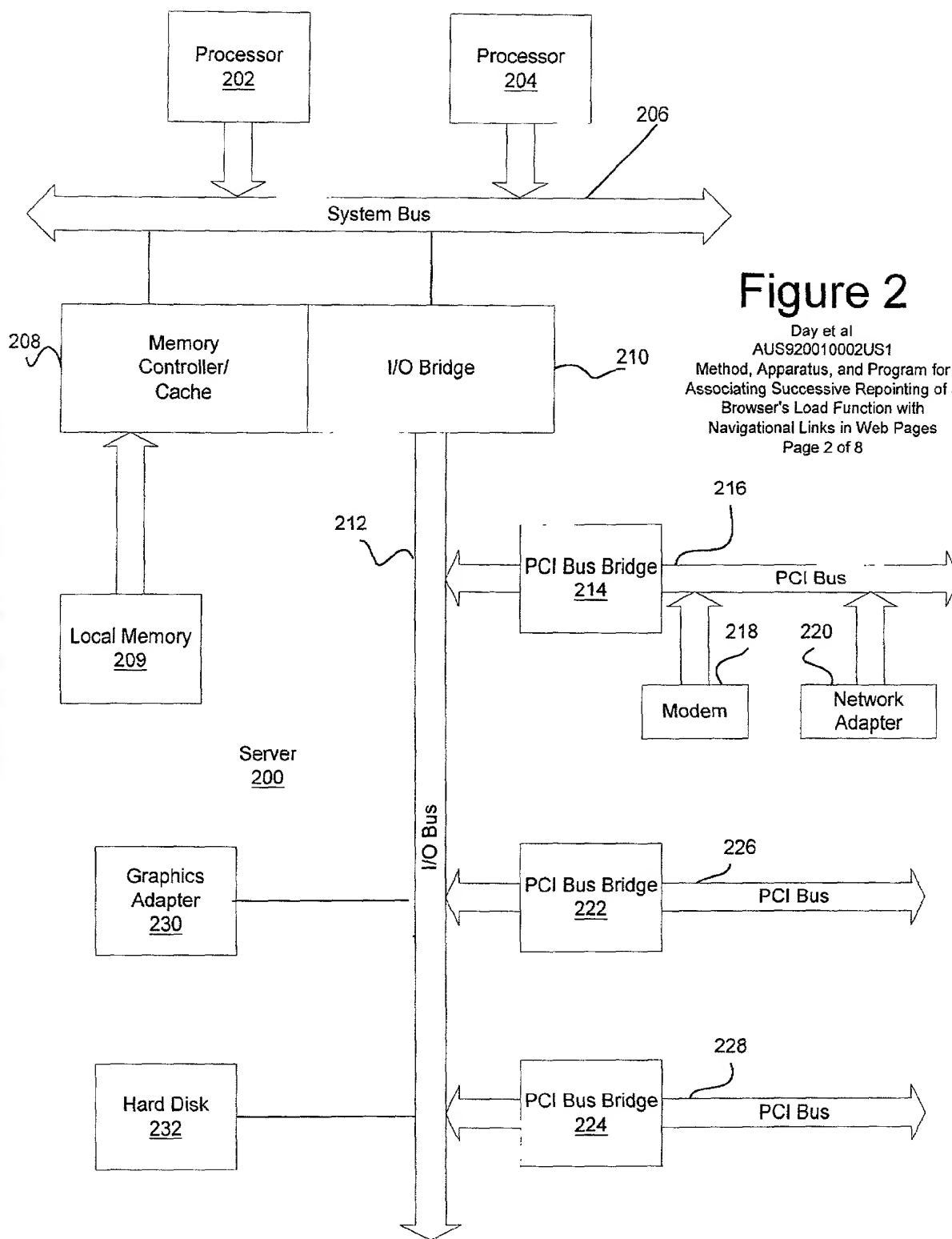


Figure 2

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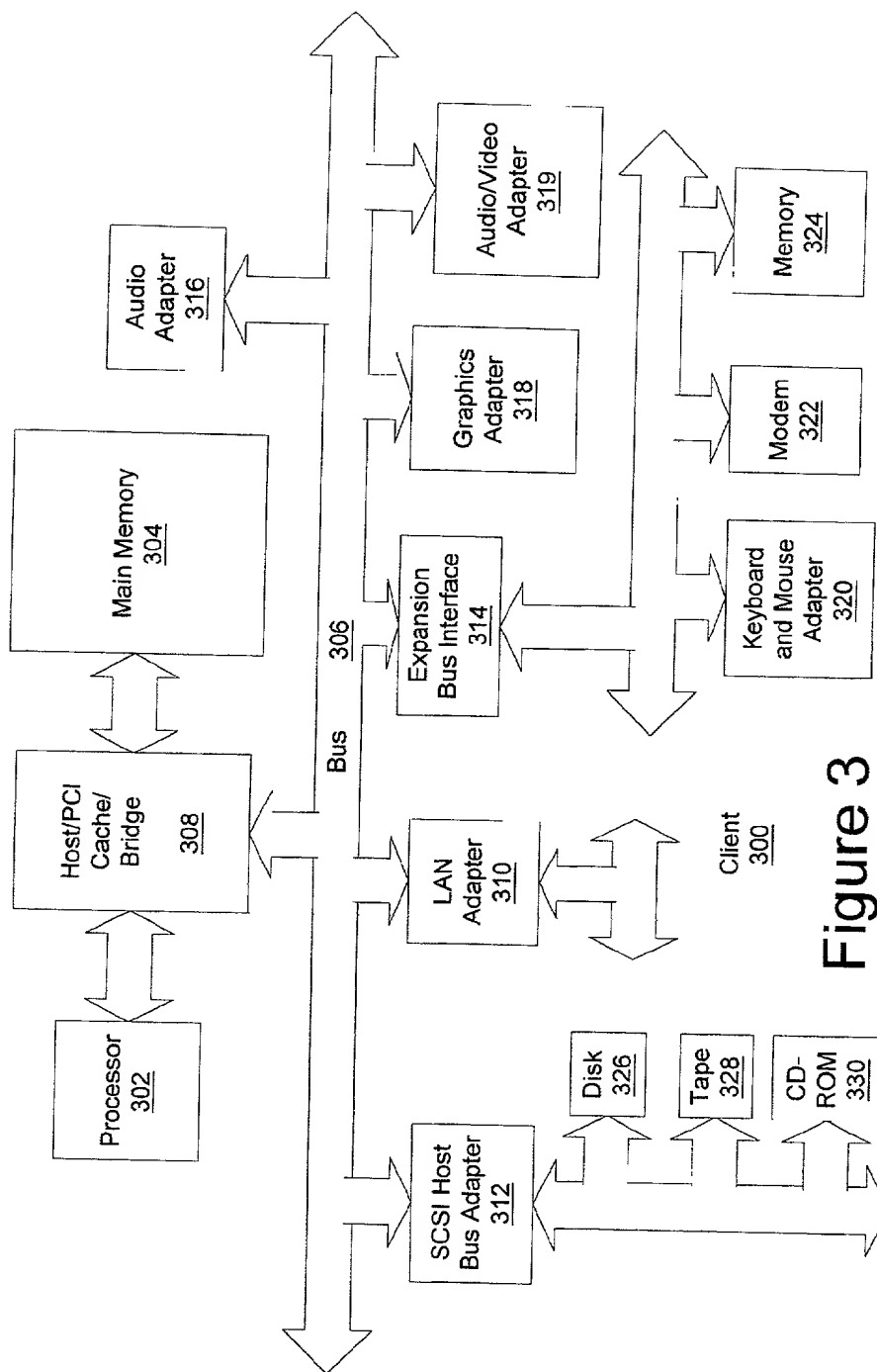


Figure 3

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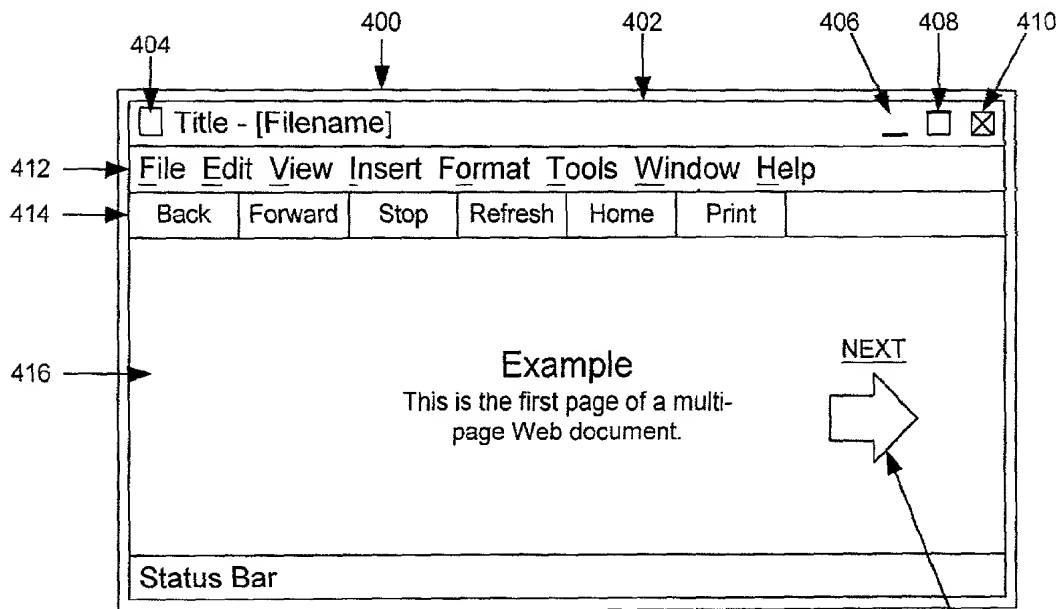


Figure 4A

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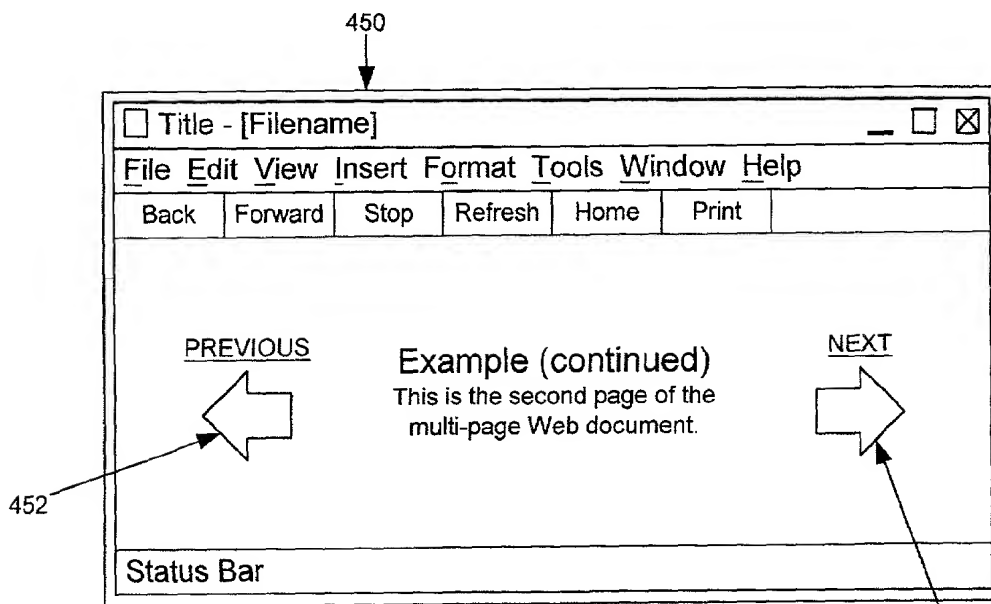


Figure 4B

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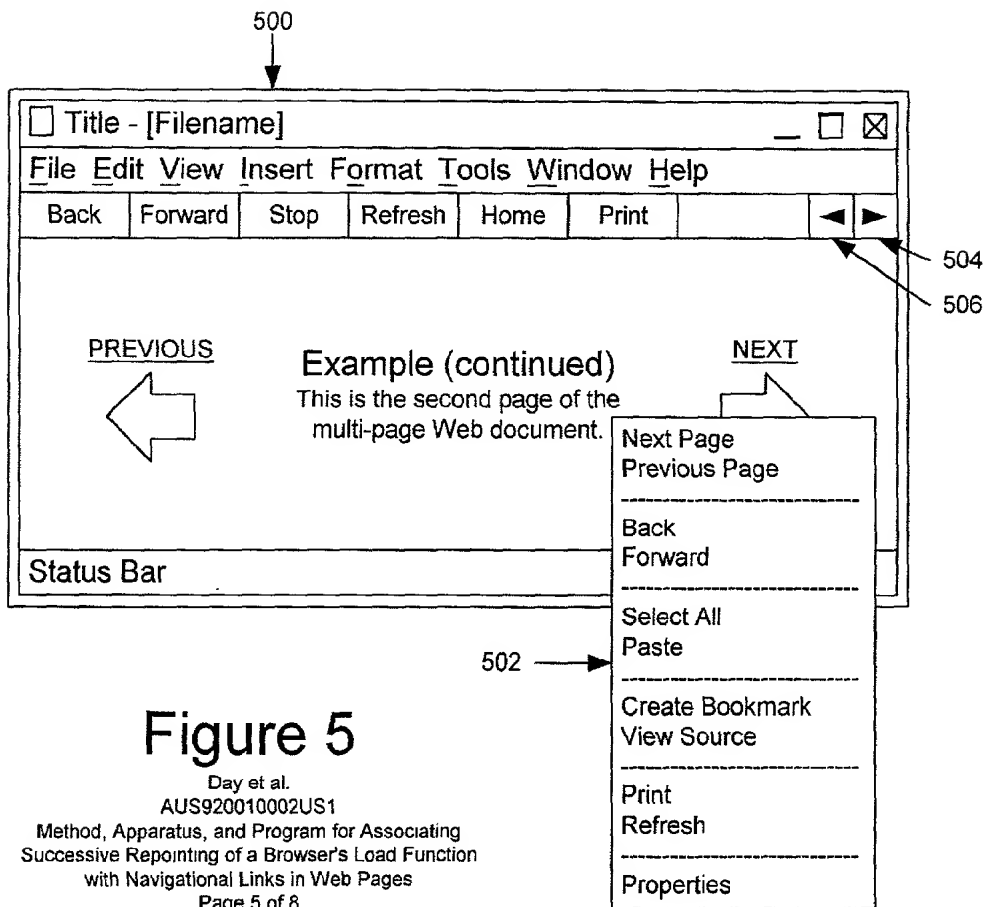


Figure 5

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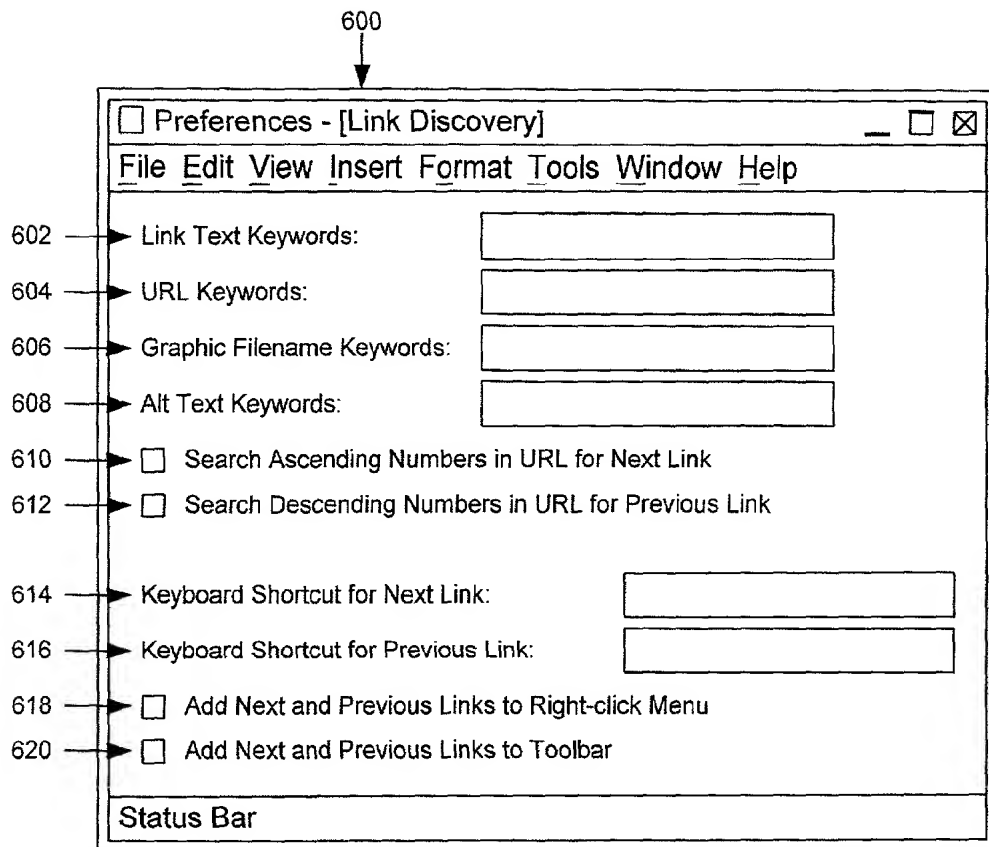


Figure 6

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Figure 7

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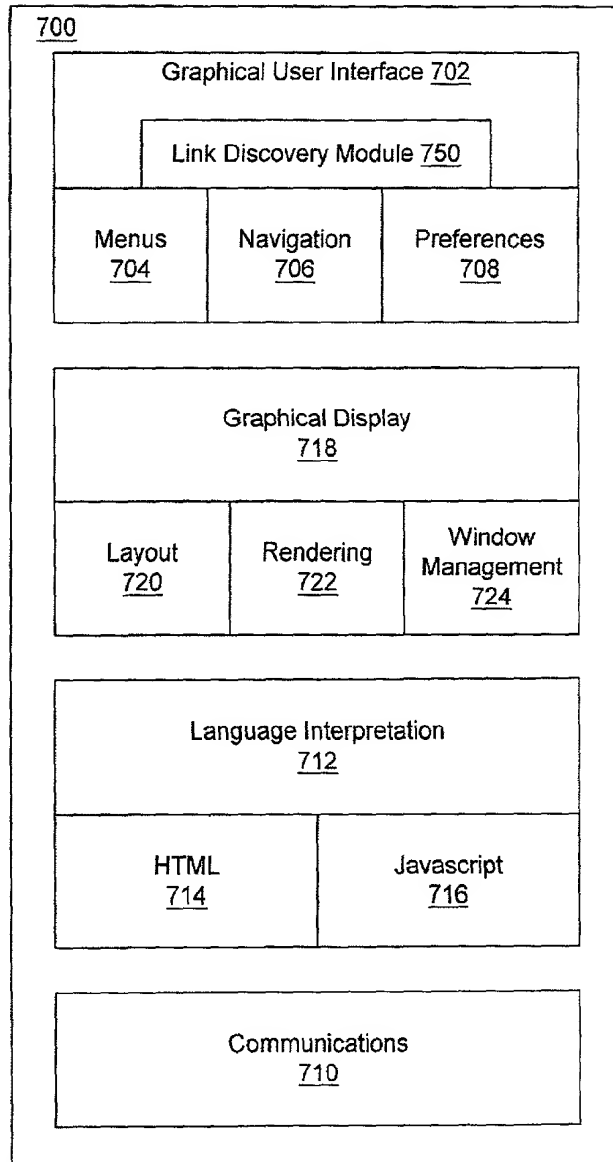


Figure 8

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